Abstract—Printed circuit board (PCB) stators in coreless axial flux permanent magnet (AFPM) machines have been proposed, designed and studied for utilization in multiple industries due to their design flexibility and reduction of manufacturing costs, volume, and weight compared to conventional stators. This paper investigated loss mechanisms and multiple methods of approximating winding supplementary losses in PCB stators with example wave and spiral winding topologies for a dual rotor, single stator configuration using 3D FEA models with a 15 hour runtime. The effect of rotor magnet placement, end windings, and active conductor path on eddy current is studied for both topologies. A hybrid FEA/analytical approach is proposed for the approximation of circulating current within layers of a planar PCB conductor using the equivalent circuit model. A coreless AFPM motor prototype is created and its power losses tested experimentally to approximate stator winding supplementary losses. Since each path is made up of a series of turns that are distributed across many layers, their corresponding back-emf will be connected arrangement is essential. Differences between layers and circulating currents. As the conductors are not the same, resulting in induced voltage depends on where the individual traces are located. Similarly distributed across many layers, their corresponding back-emf within [9], eddy current losses in the brushless motor’s PCB active conductor is, especially for high-speed AFPM. In [7], the authors derived a closed-form expression for eddy current losses within AFPM PCB stators which showed how important the width of the active conductor is, especially for high-speed AFPM [4, 8]. Within [9], eddy current losses in the brushless motor’s PCB stator have been measured using a numerical method with a single magnetostatic solution and reasonable assumptions. For rotor imbalances that result in circulating currents and power loss, the authors of [4] developed a relationship showing how connection between layers directly contributes to power losses. Since each path is made up of a series of turns that are distributed across many layers, their corresponding back-emf depends on where the individual traces are located. Similarly in [5], it was proposed that rotor flux linkage between layer conductors are not the same, resulting in induced voltage differences between layers and circulating currents. As the number of layers increases, the difference in induced voltages increases, intensifying circulating currents. Therefore, the selection of the number of layers, conductor paths, and their connected arrangement is essential.

The introduction of printed circuit board (PCB) stators in AFPM has become a trending topic due to their reduced weight and volume, ease of accurate manufacturability, and allowing for more accessible mass production. The large flexibility in PCB stator coil shape, interconnection, and implementation has led to a multitude of studies focusing on their design and optimization for maximal efficiency [1–6]. Calculation of AC losses in PCB stators is essential towards winding design optimization prior to motor production as it contains the majority of component losses [4]. In [7], the authors derived a closed-form expression for eddy current losses within AFPM PCB stators which showed how important the width of the active conductor is, especially for high-speed AFPM [4, 8]. Within [9], eddy current losses in the brushless motor’s PCB stator have been measured using a numerical method with a single magnetostatic solution and reasonable assumptions. For rotor imbalances that result in circulating currents and power loss, the authors of [4] developed a relationship showing how connection between layers directly contributes to power losses. Since each path is made up of a series of turns that are distributed across many layers, their corresponding back-emf depends on where the individual traces are located. Similarly in [5], it was proposed that rotor flux linkage between layer conductors are not the same, resulting in induced voltage differences between layers and circulating currents. As the number of layers increases, the difference in induced voltages increases, intensifying circulating currents. Therefore, the selection of the number of layers, conductor paths, and their connected arrangement is essential.

Derived from previous studies describing wave [2, 10] and spiral winding topologies [3, 4], two PCB stators were designed, simulated in ANSYS Electronics, manufactured, and experimentally tested to approximate stator winding supplementary losses. This paper focuses on winding supplementary losses, i.e., eddy current and circulating current losses, that are very important for PCB stator integration and will be

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addressed in the following sections. The eddy and circulating current loss mechanisms were studied and numerically derived through 3D FEA and analytical methods considering the impacts of rotor and coil geometry, parallel and series connections and system sensitivity to rotor asymmetry. Experimentally, spin-down tests were performed for the derivation of PCB stator related power losses considering a loss separation technique.

II. PCB STATOR TOPOLOGIES

Studied PCB stators are comprised of planar copper conductor traces distributed axially on multiple layers through plated copper through-hole vias and radially with insulation between. Due to the planar nature of these traces, connections layer to layer and radially along the PCB stator require shape alteration to connect turns and coils as maximum torque is produced by maximizing radial trace alignment.

One of the major limitations in PCB stators is current carrying-capability of the conductor traces for field generation. Connecting planes of conductors in parallel or increasing conductor cross-sectional area are two measures to address this issue with a trade-off between Joule loss and eddy current loss minimization. For instance, stator eddy current losses are greatly reduced compared to conventional coreless AFPM machines due to the very small conductor cross-sectional area [4]. Current capability, however, is decreased with conductor area reduction and leads to parallel connections between traces to mitigate Joule losses in single conductors. Introduction of parallel paths for multi-layer connections increases the likelihood of circulating current generation between traces of different induced voltages within PCB layers, causing rotor imbalances [5]. These factors have led to many alternative approaches and designs for winding coil shapes and interconnections in previous works and industry [11], all of which boast varying electromagnetic properties greatly impacting machine performance.

The wave-type PCB stator winding, shown in Fig. 1(a), based on the topology explored in [10, 12] and optimized considering eddy losses in prior work [2], comprises 12 layers, 10 which contain active copper traces and two that are used as a path to route the return. On each active layer, there are 42 traces with a 0.14mm trace thickness, \( T_t \), 0.2mm trace width and 0.25mm isolation width, grouped in six planar parallel traces aggregated in series to form seven turns, a pattern which is repeated for all active layers, as summarized in Fig. 4. The equivalent number of turns per phase, \( N_t \), is detailed in Table I for the wave winding from the product of the number of turns in one layer, seven, and the number of poles, 26. Vias are used to connect layers in parallel to increase current-carrying capability and apply system-balancing forces when facing rotor imbalances. The wave winding configuration maximizes active radial length, can host many pole pairs, and allows for easy layer stacking, however the turns are constrained by the outer and inner radii and manufacturing precision [10].

The spiral-type PCB stator winding, depicted in Fig. 1(c) described previously in [3, 4, 13], comprises six layers, all of which are active with a two layer coil pattern that radially connects the input and output terminals. On each active layer, there are 26 coils and each coil has 27 turns with an altered 0.07mm trace thickness with other system parameters summarized in Table I. Both configurations use a 26 pole rotor, shown in 1(b) on both sides to complete the flux path through the stator. Vias are used to connect all traces in series within one coil between layers and radially connect coils around the circumference via input/return bus bars. The spiral configuration maximizes coil area utilization, allows for a greater number of turns, and maximizes the torque to copper ratio, however it generates noncontributing torque due to angle relative to the motor center, and the active length is significantly shorter for inner tracks/turns [7]. FE models and physical prototypes of both PCB stators and the 26 pole double sided rotor are presented in Fig. 1.

III. WINDING LOSS MECHANISMS

One of the most critical factors in coreless electric machine design is avoiding or reducing eddy current losses in stator windings. Without the protection of slots, windings in coreless
AFPM machines are directly exposed to airgap flux density variations, causing eddy current within the stator’s planar conductors, generating heat and non-contributing electromagnetic force\[14\]. Power dissipation of these eddy currents are dependent on the wire dimensions, material constants, and the operating frequency. For a PCB stator consisting of traces with a rectangular cross section, eddy current losses can be calculated \[15\]

\[ P_{\text{eddy}} = \frac{\pi^2 N_c N_t f^2 t_u t_h l_m}{6\rho} (t_u^2 B_z^2 + t_h^2 B_\phi^2) \]  \tag{1} \]

where \(N_c\) is number of coil sides with average length of \(l_m\), and \(N_t\) turns per coil. \(B_z\) and \(B_\phi\) are axial and tangential components of the flux density, respectively. This equation also shows how PCB trace geometry affects eddy current losses. Where \(t_u\) is trace width and and \(t_h\) is trace height, in the z direction, and \(f\) denotes the frequency of flux variations.

The wave winding PCB stator, previously simulated and experimentally tested in\[2\], experiences eddy current losses caused by the shifting magnetic flux as indicated in Fig. 2(a). From the initial designs, copper trace dimension minimization and parallel paths were the focus for eddy current loss reduction\[2\]. The eddy current path, shown in Fig. 2(b), is constrained to the radial conductor, preventing circulation through the system through the end winding as there is no flux nor current density. The 4.43mm copper trace skin depth is much larger than the conductor cross section of 0.2mm by 0.14mm with a 216Hz frequency at 1000rpm, greatly reducing eddy current power losses.

The flux density mapping of the spiral winding in Fig. 3(a) highlights that the rotor magnets fully cover the end winding during operation to utilize maximal active conductor but also extending a full eddy current path through a coil. Since there are more conductors covered by the rotor magnets at any point in time, there is more resistive heating and eddy current flows easily through the system. Vias within the center of the coils are used for traveling between layers and coil sections, allowing eddy current to generate and move between layers within the varying flux density region, as shown in Fig. 3(b). The detailed view shows how eddy current travels back and forth within the traces, similar to that of the wave winding with the derived eddy current loss reported in Table II.

Circulating currents within the stator winding is caused primarily by voltage differences between parallel paths. Equation (2) is a general expression for the calculation of circulating current losses within n parallel path with the equal resistance
Fig. 4. Wave winding PCB stator diagrams including the conductor cross section for one phase and equivalent circuit for one layer (a), the open-circuit circulating currents in parallel traces within one turn (b), and a heat-map of the calculated induced voltages between copper traces within one turn over ten layers (c).

Fig. 5. Spiral winding PCB stator equivalent circuit diagram (a). Parametric studies for misalignment and rotor magnet properties for circulating current losses with varying voltage difference and voltage angle (b) and misalignment of the rotor (c) resulting in an approximate circulating current loss.

Any rotor asymmetry in magnetic and geometric parameters can induce circulating currents within both PCB stators, of which the spiral design has much greater sensitivity. Parallel layers paths were used with typical manufacturing dimensions in our wave winding PCB design to test our proposed system of analysis for circulating current losses. Parallel paths reduce the conductor minimum size limitations due to Joule losses, approximated as resistances times current squared, while also creating a balancing magnetic force when imbalanced rotors are applied within the AFPM machine.

The circulating current power loss of the wave winding design can be approximated analytically using (2) based on the stator equivalent circuit shown in Fig. 4(a). Considering the 3D FEA results for rms circulating current in each trace, shown in Fig. 4(b), and the resistance of each trace, as a function of length, resistivity, and cross-sectional area, the calculated total circulating current power loss in all 420 traces is 28.3 W, as reported in Table II.

IV. SIMULATION AND EXPERIMENTAL RESULTS

Having designed, analyzed, and simulated the two PCB stator designs, an experimental AFPM synchronous motor prototype was created as shown in Fig. 6(a). Spin down tests...
were performed with rotor speed spun externally to a steady 1000rpm at which point the prime mover was decoupled and motor deceleration was measured with each PCB stator type. A plastic disc the same diameter as the PCB stator, similar to a process performed in [4, 16], was used to separate mechanical and electromagnetic loss components. The resulting power loss from all 3 cases is shown in Fig. 6(d).

Resulting losses from the experimental and FEA/analytical approximation methods are shown in Table II with the experimental losses matching the sum of circulating and eddy losses predicted. Circulating current losses within these designs were estimated to consist of majority of the losses present in the system. Computational and experimental analysis of circulating current losses, using the models shown in Fig.6(b) and (c), indicate that series connections should be made within PCB stator windings wherever possible to minimize current flowing between conductors of different induced voltages.

V. Conclusion

This paper investigated loss mechanisms for coreless AFPM synchronous machines integrating PCB stators with wave and spiral winding designs. For the studied windings, power loss components were individually approximated based on detailed 3D FE models and analytical methods.

Eddy current path limitations were studied to reduce eddy current loss in future PCB stator optimization and designs. Circulating current loss estimation methods have been developed to approximate power loss in varying planar conductors. Furthermore, rotor asymmetry was found to contribute to losses and estimated using parametric equivalent circuit analysis. Open-circuit spin down tests were used to experimentally assess PCB stators’ power losses, and the results were found to be comparable to FE-based analytical methods.

Within this study, it was found that optimization of coil geometry can reduce eddy losses significantly, however, parallel layers, used to improve current carrying capability and Joule losses, can greatly increase circulating currents and associated losses. Accurate methods of both eddy and circulating current loss approximation are essential to minimize AC supplementary copper losses.

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